

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Sergey Sukhobok, et al.	Examiner:	Unassigned
Serial No.:	Unassigned	Art Unit:	Unassigned
Filed:	Herewith	Attorney Docket No.:	16811MDUS01U
Title:	ALGEBRAIC LOW-DENSITY PARITY CHECK CODE DESIGN FOR VARIABLE BLOCK SIZES AND CODE RATES		

M.S. Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

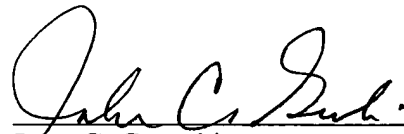
**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Applicants respectfully request consideration of the references submitted herewith and listed on the attached PTO-1449. The Examiner is respectfully requested to initial next to each reference indicating that it has been considered and return a copy of the PTO-1449 to applicant with the first action on the merits in this application.

No fees are believed due in connection with this filing. If any fees are due in connection with this filing, the Commissioner is hereby authorized to charge payment of the fees associated with this communication or credit any overpayment to Deposit Account No. 502246 (Ref. NN-16811).

Respectfully Submitted

  
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John C. Gorecki  
Registration No. 38,471

Dated: March 11, 2004

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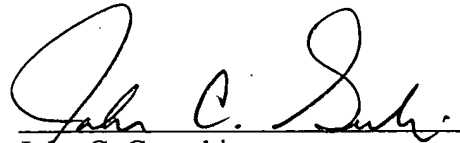
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Substitute for Form 1449A/PTO		Application No.	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Filing Date	March 11, 2004
		First Named Inventor	Surgey Sukhobok, et al.
		Art Unit	
		Examiner	
Sheet 1 of 1		Attorney Docket No.	16811MDUS01U

U.S. PATENT DOCUMENTS				
Examiner's Initials	Citation Number	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document
	A1			
	A2			
	A3			
	A4			
	A5			

FOREIGN PATENT DOCUMENTS				
Examiner's Initials	Citation Number	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document
	B1			
	B2			

OTHER PRIOR ART -- NON-PATENT LITERATURE DOCUMENTS				
Examiner's Initials	Citation Number	Document Description -- Include the name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	C1	R. ECHARD, et al., <i>The <math>\pi</math>-Rotation Low-Density Parity Check Codes</i> , IEEE publication 0-7803-7206-9/01, 2001, pp. 980-984		
	C2	R. ECHARD, et al., <i>Irregular <math>\pi</math>-Rotation LDPC Codes</i> , IEEE publication 0-7803-7632-3/02, 2002, pp. 1274-1278		
	C3	R. ECHARD, et al., <i>The Extended Irregular <math>\pi</math>-Rotation LDPC Codes</i> , IEEE communications letters, Vol. 7, No. 5, May 2003, pp. 230-232		
	C4	R. ECHARD, et al., <i>Deterministic <math>\pi</math>-Rotation Low-Density Parity Check Codes</i> , Electronics Letters, Vol. 38, No. 10, May 2002, pp. 464-465		
	C5	D. HOCEVAR, <i>LDPC Code Construction with Flexible Hardware Implementation</i> , IEEE publication 0-7803-7802-4/03, 2003, pp. 2708-2712		
	C6	H. ZHONG, et al., <i>Design of VLSI Implementation-Oriented LDPC Codes</i> , (4 pages)		
	C7	R. ECHARD, <i>On the Construction of Some Deterministic Low-Density Parity-Check Codes</i> , Ph.D. dissertation, 2002, (162 pages)		

Examiner Signature		Date Considered	
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.